

Systems and Methods for Ramping Power Amplifier Output Power

ABSTRACT OF THE DISCLOSURE

A power amplifier circuit including a first transistor, a second transistor, a power control circuit and a power control circuit. The first transistor includes a first input and a first output. The second transistor includes a second input coupled in series with the first output of the first transistor. The input circuit is coupled to the first input of the first transistor. The power control circuit is coupled to the second input of the second transistor. The control circuit includes a time delay circuit and a variable source.